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December 18, 2006

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Examiner Kiesha L. Rose Commissioner for Patents

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Client-Matter Number:

351913-992471

From:

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U.S. Serial No.: 10/797,296

Pages: - 10 - (including this form)

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Transmitted via Fax on: December 18, 2006 Attorney Docket No. 351913-992471

U.S. Serial No.: 10/797,296 Filing Date: March 9, 2004

BURIED BIT LINE NON-VOLATILE FLOATING GATE MEMORY CELL WITH INDEPENDENT CONTROLLABLE CONTROL GATES IN A TRENCH, AND ARRAY THEREFORE, AND METHOD OF FORMATION

Enclosed is:

1) Response to Office Action of September 19, 2006.

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DEC 182006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.

10/797,296

Applicant

Dana LEE

Filed

March 9, 2004

TC/A.U.

2822

Examiner

Kiesha L. ROSE

Title

BURIED BIT LINE NON-VOLATILE FLOATING GATE

MEMORY CELL WITH INDEPENDENT

CONTROLLABLE CONTROL GATES IN A TRENCH, AND ARRAY THEREFORE, AND METHOD OF FORMATION

Docket No.

351913-992471 (Formerly 2102397-992471)

Customer No.

26379

Mail Stop Amendment Commissioner for Patents P.O. Box 1450

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Rosa A. Caviedes

RESPONSE TO OFFICE ACTION OF SEPTEMBER 19, 2006

Sir:

In response to the Office Action of September 19, 2006, Applicants hereby submit the following remarks and arguments traversing the rejection of the pending claims.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

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